



# Fundamentals of Digital Logic Design With VHDL

*PhD, PE, M.H. Hassan*

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## **Fundamentals of Digital Logic Design With VHDL** PhD, PE, M.H. Hassan

This book provides analysis and design of digital circuits and systems. It introduces digital design from basic concepts to advanced circuits and systems using both theoretical and CAD supported methods. The book gives an introduction to VHDL throughout with a large number of examples and case studies. Key features

- Covers the analysis and design of combinational networks using Boolean algebra and K-maps
- Presents complete coverage to the analysis and design of sequential networks
- Places a strong emphasis on developing and using systematic procedures
- Includes a thorough coverage to VHDL at the end of each chapter
- Contains in-depth presentation of modern digital system design using programmable-logic devices
- Comprises detailed solved examples in every chapter
- Incorporates practical problems for the students/readers to carry out

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